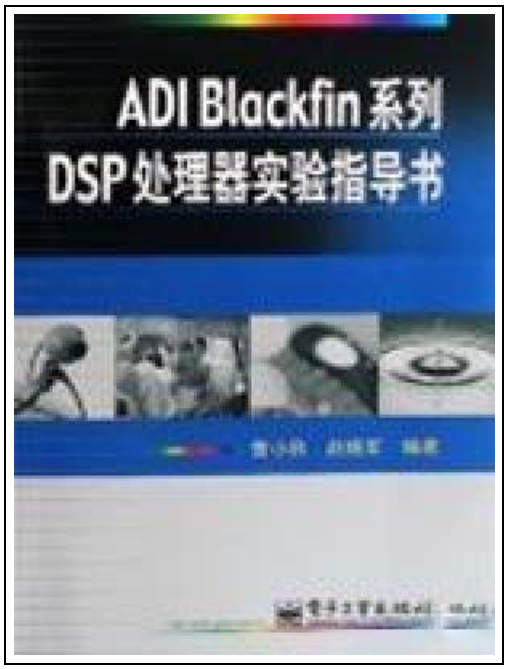
ADI BLACKFIN series DSP processors experimental instructions



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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Publisher: Electronics Industry Publishing House Pub. Date :2010-09-03. Book Blackfin processor-based guidance of a full range of experimental materials. The main contents include DSP processor chip overview of the experimental hardware platform ADSP-BF533 EZ-KIT Lite to use. USB-LAN expansion board uses. EBF-533 digital audio and video test system for the use of software development tools. VisualDSP + + 4.0 Introduction and application. JTAG emulator to use. the kernel of basic operations. BF533 processor addressing modes and data processing instruction experiments. embedded development based interfaces and peripherals. the base 4-FFT algorithm implementation on the ADSP-BF533. fast Fourier Ye inverse transform (IFFT) algorithm. finite impulse response FIR digital filters. IIR filters to achieve. DCT algorithms. program optimization and operating systems. We hope that readers through a full range of the Blackfin processor experiments. to further deepen the understanding of the Blackfin processor to improve the Blackfin processor project research and development capabilities. Contents: Chapter 1 Overview 1.1 Blackfin DSP processor chip. processor introduction 1.2 Introduction 1.2.1 ADI s other processors and other processors 1.2.2 Chapter 2. teaching other processor system platform 2.1 ADSP-BF533 EZ-KIT Lite s use the 2.2 USB-LAN expansion board with 2.3 EBF-533 digital audio and video experiment development system development tools Chapter 3 3.1 VisualDSP + + 4.0 Introduction 3.2 VisualDSP + +3.54.0 Application 3.3 JTAG emulator user manual 3.3.1 JTAG emulator Introduction 3.3.2 JTAG emulator with basic operations in Chapter 4 4.1 kernel registers the cell structure of the system characteristics and Blackfin processors feature 4.2 ALU arithmetic logic instructions programmed 4.3 ADSP-BF533 multiply - shift accumulator 4.4 Blackfin instruction and 4.5 DSP-bit instructions to learn division 4.6 BF533 processor addressing modes and data processing instructions embedded...

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