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## OPTIMIZATION OF FDSOI MOSFET BY USING GROUND PLANE AND BI AXIAL STRAIN



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Condition: New. Publisher/Verlag: LAP Lambert Academic Publishing | Concept of ground plane and strained silicon , design of Fdsoi Mosfet and optimization for leakage & driving current | Continued miniaturization of bulk silicon CMOS transistors is being limited by degrading short channel effects. However, these techniques are rapidly approaching material and process limits. Alternate transistor architectures such as the planar ultra-thin body (UTB) FET and double-gate MOSFET may be necessary to continue gate length scaling down to the sub-10nm regime but...

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