DIGITAL INTEGRATED CIRCUIT DESIGN

Working from the fundamentals of transistor-level design and building up to system-level considerations, Digital Integrated Circuit Design shows students with minimal background in electronics how to design state-of-the-art high performance digital integrated circuits. Ideal as an upper-level undergraduate text, it can also be used in first-year graduate courses and as a reference for practicing engineers.

Salient Features

· Presents transistor-level details first, building up to system considerations

· Emphasizes CMOS technology but also includes in-depth explanations of designing in bipolar, BiCMOS, and GaAs technologies

· Features modern, well-designed examples and problems

· Covers important system-level considerations such as timing, pipelining, clock distribution, and system building blocks in detail

· Discusses key elements of semiconductor physics, integrated circuit processing, transistor-level design, logic-level design, system-level design, testing, and more

· Provides physical and intuitive explanations throughout

· Emphasizes conceptual thinking and design methodology over detailed circuit analysis techniques.

About the Author

Ken Martin is the Stanley Ho Professor of Microelectronics in Electrical and Computer Engineering, University of Toronto. He is co-author, with David Johns, of the very popular Analog Integrated Circuit design.

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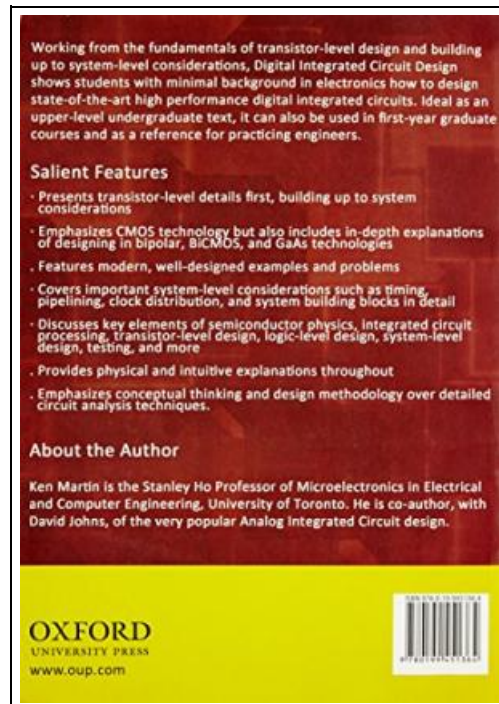
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Reviews

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(Mr. Milford Jakubowski IV)

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