



Ultracomputer Packaging and Prototypes (Classic Reprint) (Paperback)

By Ronald Bianchini

Forgotten Books, United States, 2015. Paperback. Condition: New. Language: English . Brand New Book ***** Print on Demand *****. Excerpt from Ultracomputer Packaging and Prototypes This paper reviews interconnection networks and describes packaging of the NYU Ultracomputer. The packaging is based on the button technology developed by TRW and adopted by MCC for its ES-Kit modules. This paper presents the basic ES-Kit modules needed to assemble several sizes of Ultracomputers. It also reviews Ultracomputer prototypes and previews future prototypes. The Ultracomputer is a shared memory multiprocessor computer (Figure 1.1). Since the beginning of the NYU Ultracomputer project in 1979, packaging of the interconnection network has been the fundamental limitation to the maximum realizable size and performance of an Ultracomputer. In Wireability of an Ultracomputer, the wiring of a 4096 processor (PE) Ultracomputer was described (Figures 1.2 and 1.3). About the Publisher Forgotten Books publishes hundreds of thousands of rare and classic books. Find more at This book is a reproduction of an important historical work. Forgotten Books uses state-of-the-art technology to digitally reconstruct the work, preserving the original format whilst repairing imperfections present in the aged copy. In rare cases, an imperfection in the original, such as a blemish or...



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